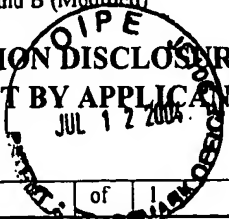


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FORM PTO-1449/A and B (Modified) INFORMATION DISCLOSURE STATEMENT BY APPLICANT 		APPLICATION NO.: 10/807,778		ATTY. DOCKET NO.: T0461.70032 US01	
		FILING DATE: 03-24-2004		CONFIRMATION NO.: 4278	
		APPLICANT: Thomas Paul Kearney			
		GROUP ART UNIT: 2819		EXAMINER: <i>JEAN PIERRE</i>	
Sheet	1	of	1		

U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
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		Office/Country	Number	Kind Code			
<i>PIP</i>		EP	0935195		Analog Devices, Inc.	08-11-1999	
<i>PIP</i>		WO	99/37027		Microchip Technology Inc.	07-22-1999	

OTHER ART -- NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)	
<i>PIP</i>		International Search Report, 1999.		
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EXAMINER: <i>P. JEAN PIERRE</i>	DATE CONSIDERED: <i>7/13/04</i>
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		FILING DATE: March 24, 2004	CONFIRMATION NO.: 4278
		APPLICANT: Thomas Paul Kearney	
		GROUP ART UNIT: 2819	EXAMINER: JEAN PIERRE
Sheet	1	of	1

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Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
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7/P		McCreary, J.L. and Gray, P.R., "All-MOS charge redistribution analog-to-digital conversion techniques - Part 1", IEEE J. Solid State Circuits, pp. 312-320, 1975.		

EXAMINER R. Jean Pierre	DATE CONSIDERED 9/13/04
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